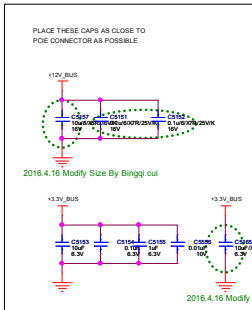
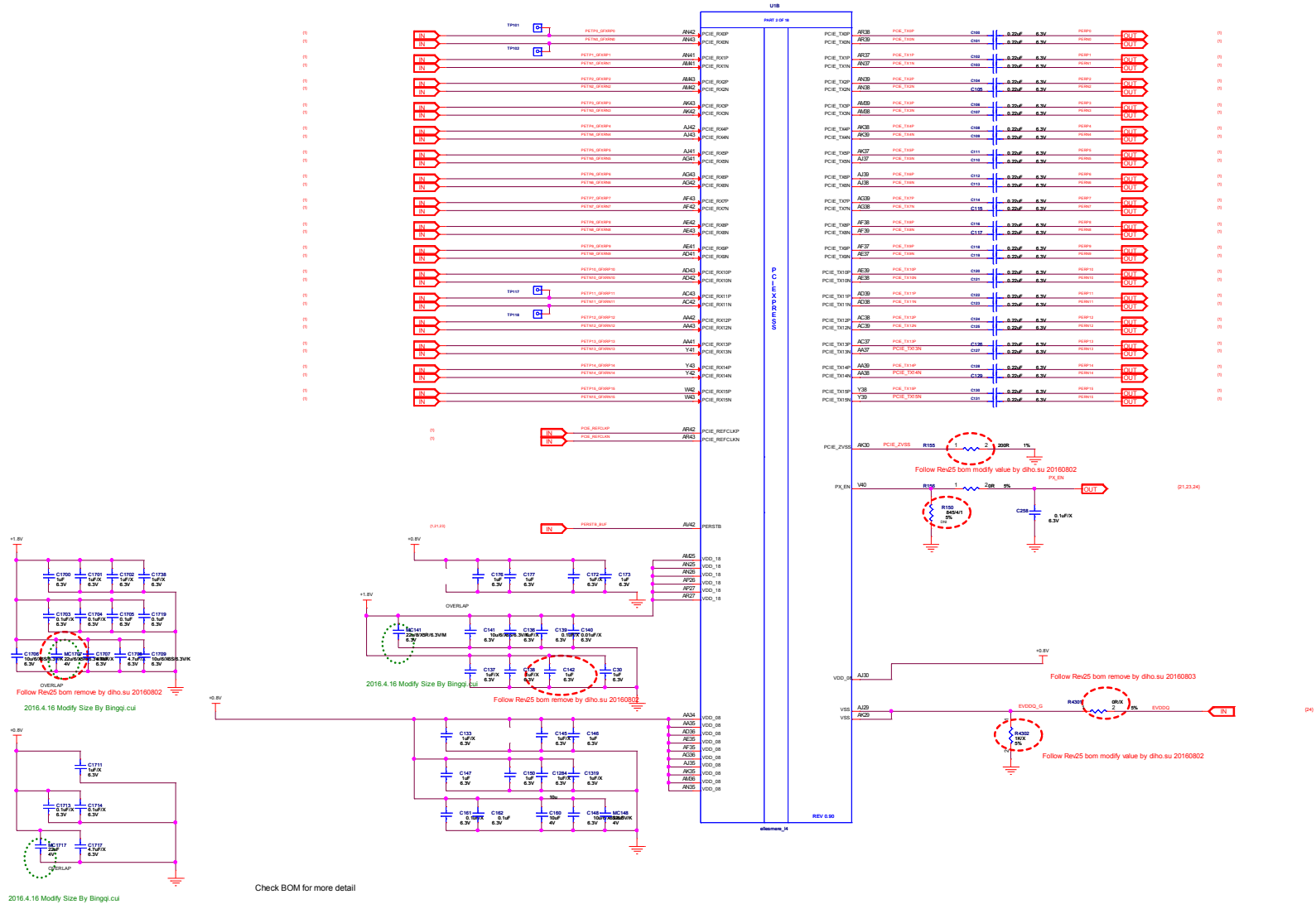


### (1) PCI-EXPRESS EDGE CONNECTOR



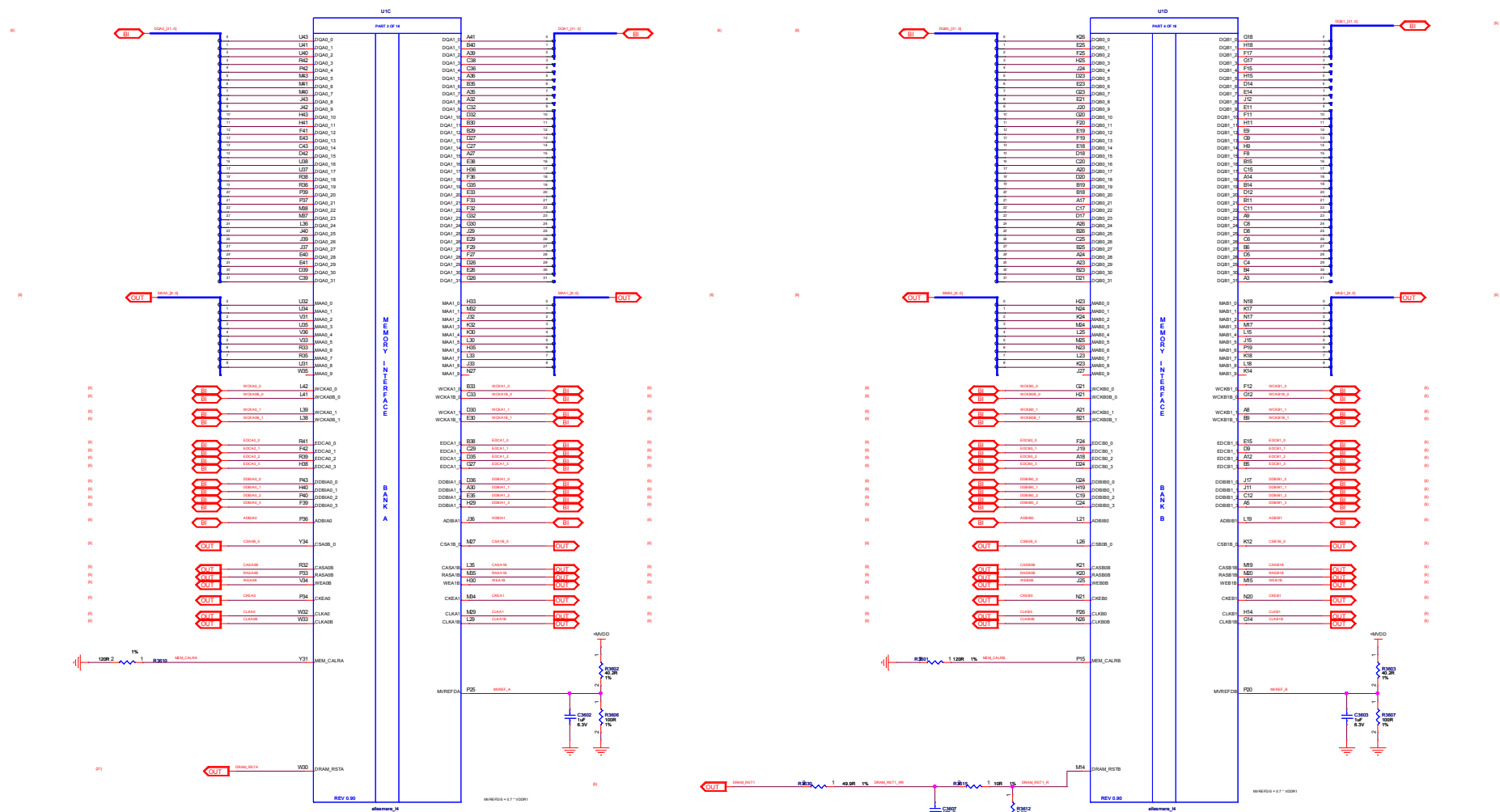
<b>GIGABYTE</b>			
Title <b>PCI Express</b>			
Size	Document Number	Rev	
Custom	<b>GV-RX580GAMING-8GD</b>	1.0	
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## (2) ELLESMERE PCIE INTERFACE

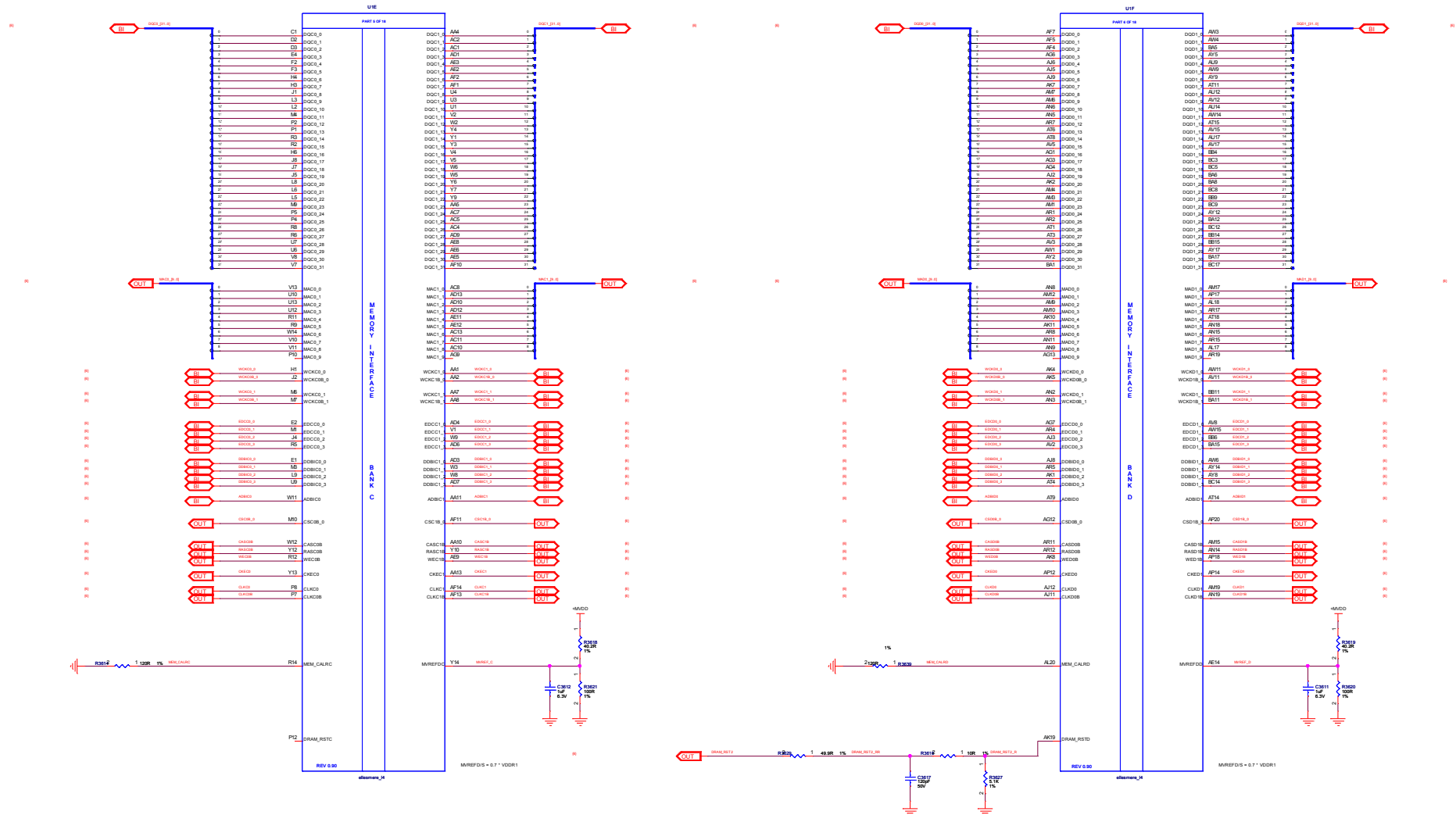


<b>GIGABYTE</b>			
Title <b>ELLESMERE PCIE INTERFACE</b>			
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(3) ELLESMERE MEM INTERFACE CH A/B

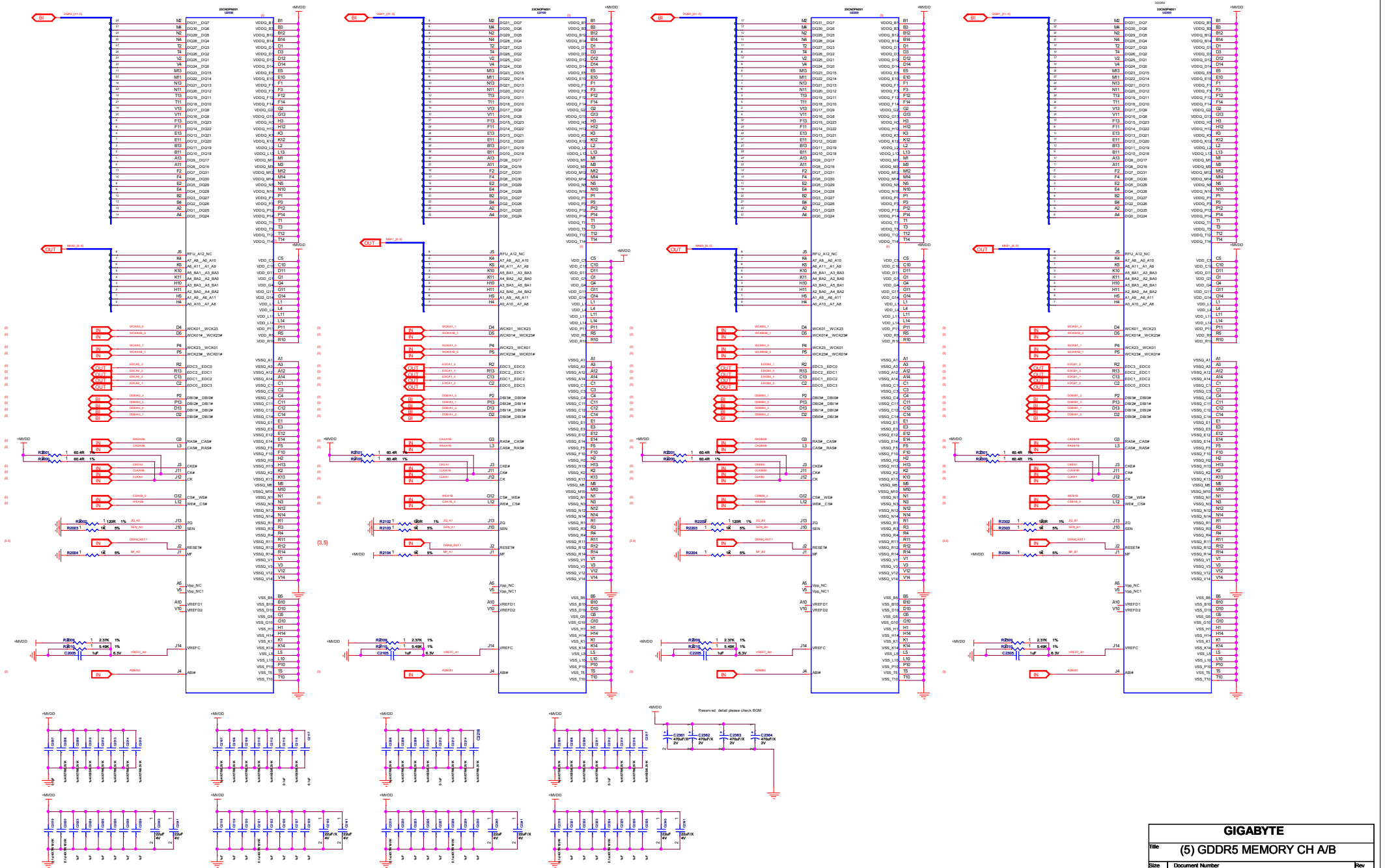


#### (4) ELLESMERE MEM INTERFACE CH C/D

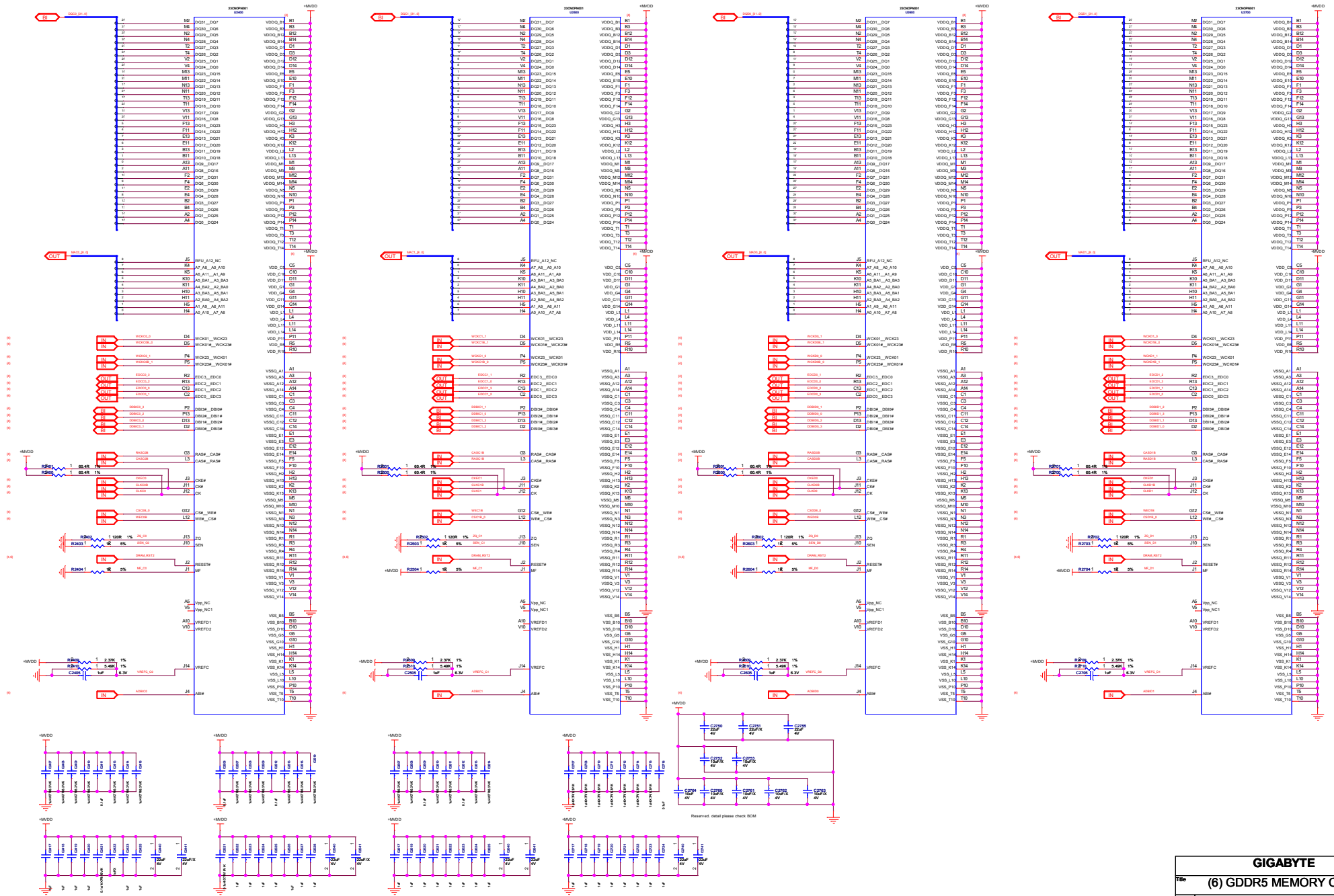


GIGABYTE			
Title	ELLESMERE MEM INTERFACE CH C/D		
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(5) GDDR5 MEMORY CH A/B



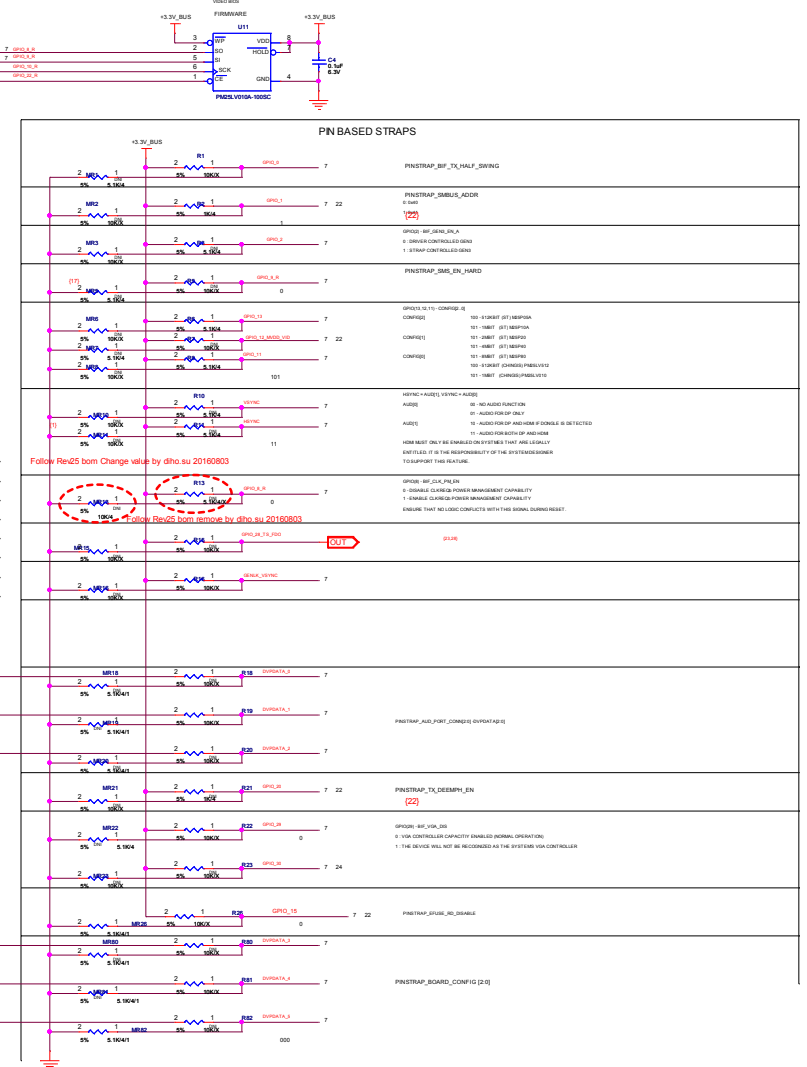
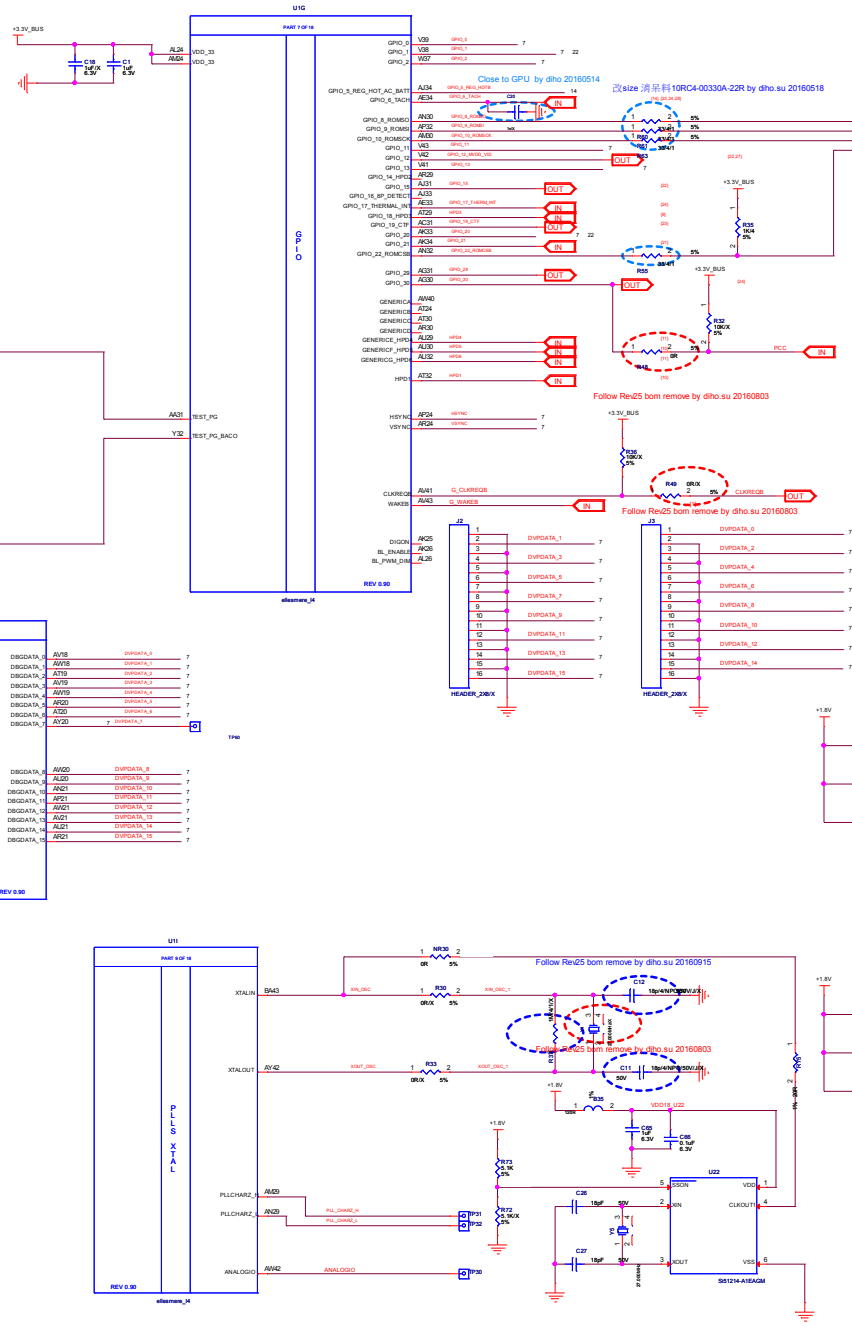
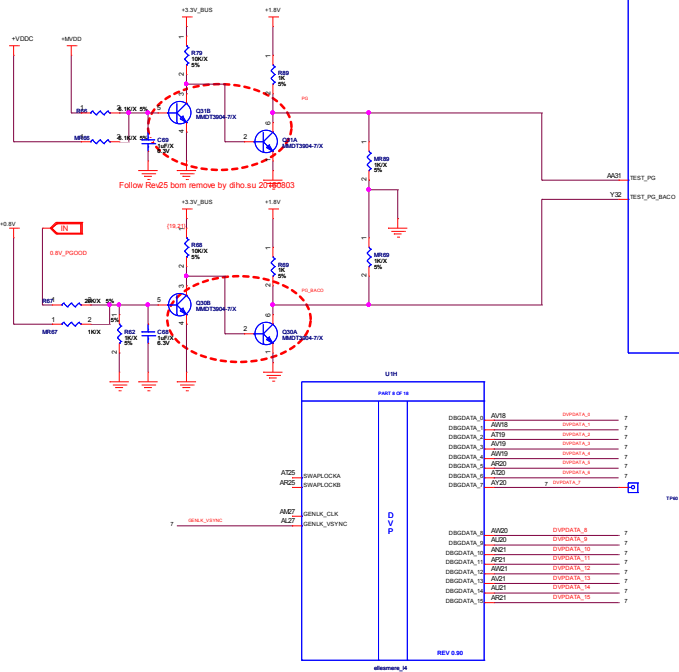
## (6) GDDR5 MEMORY CH C/D



(7) ELLESMERE GPIO STRAP CF XTAL

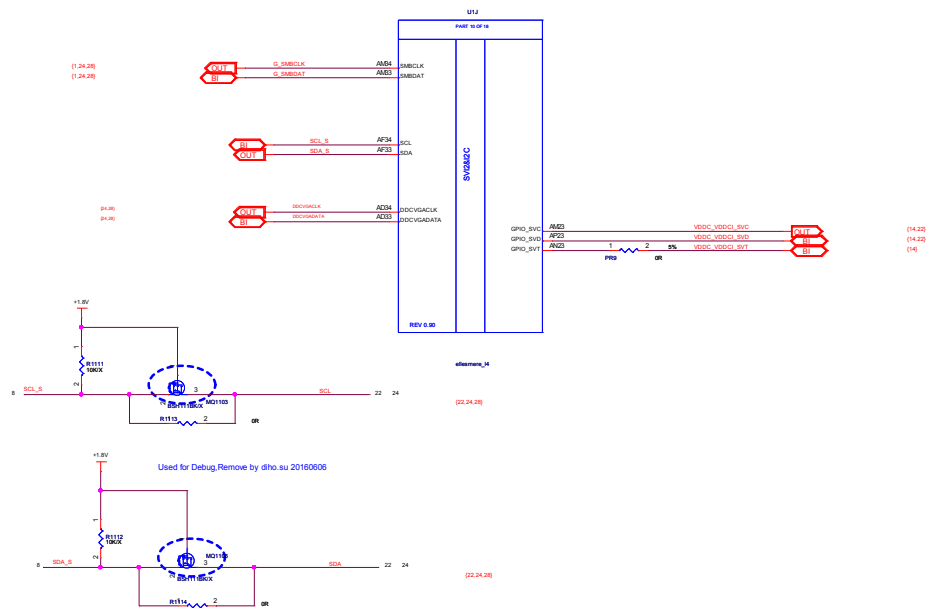
I2C/SDA BUS:		
I2C ADDRESS	FUNCTION	DEVICE

I2C ADDRESS	FUNCTION	DEVICE
0x98	EXT TEMP SENSOR	LM80063



<b>GIGABYTE</b>			
Title ELLESMERE GPIO STRAP CF XTAL			
Size Custom	Document Number <b>GV-RX580GAMING-8GD</b>		Rev 1.0
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(8) ELLESMERE DAC1 LOCK

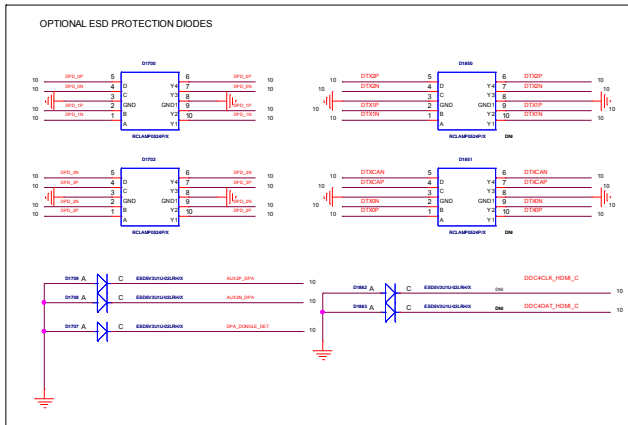
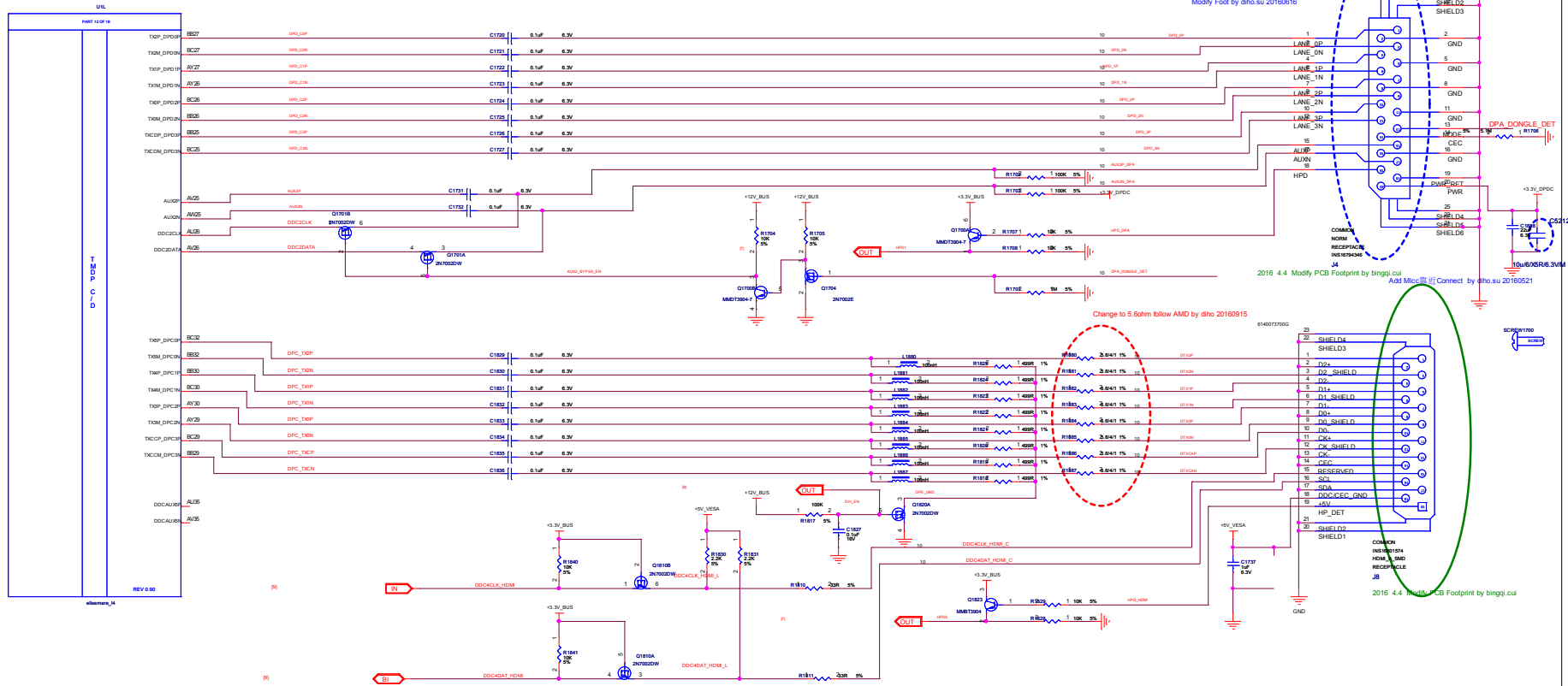


<b>GIGABYTE</b>			
Title <b>ELLESMERE DAC1 LOCK</b>			
Size	Custom	Document Number	Rev
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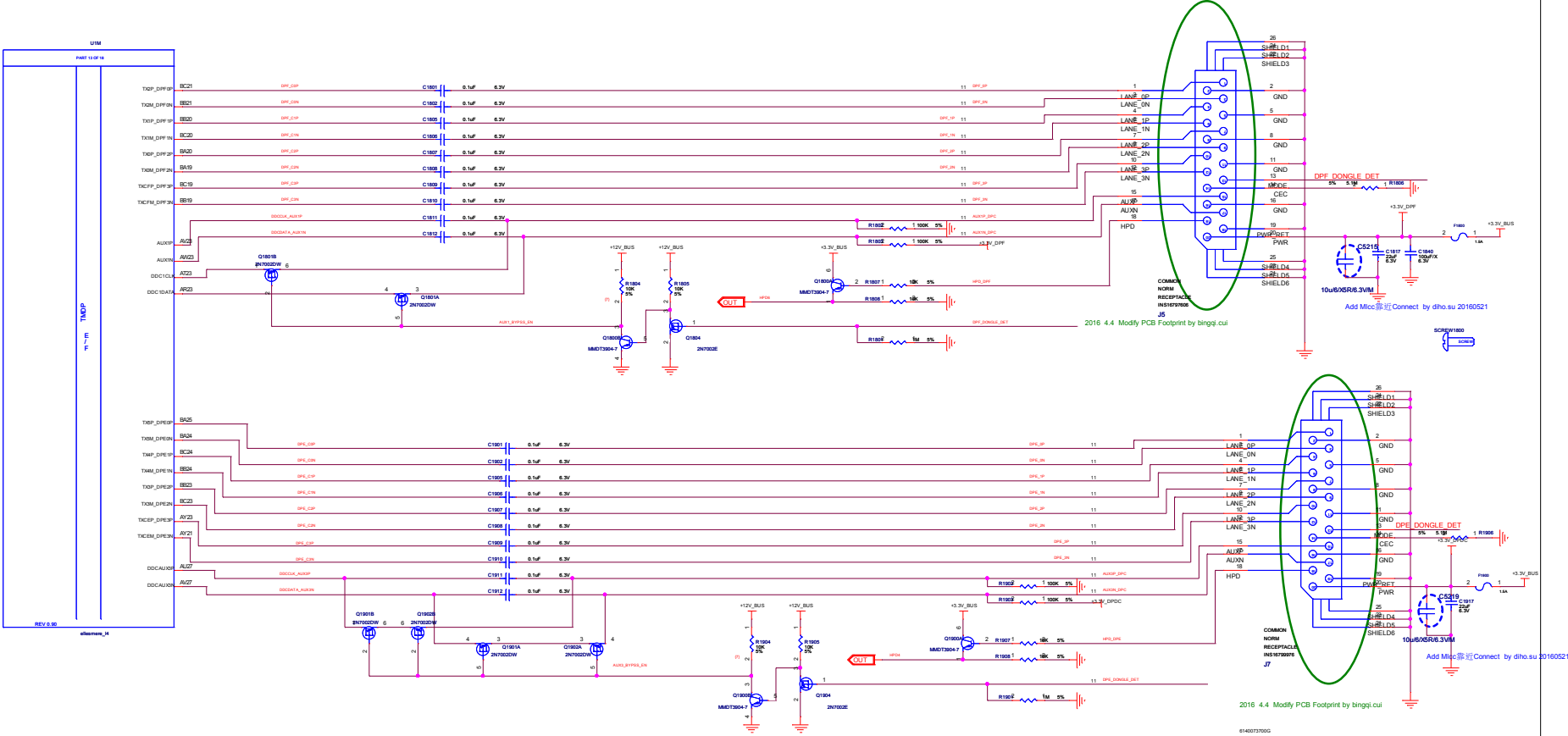




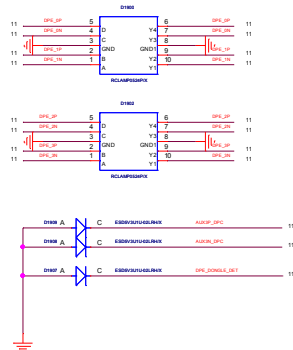
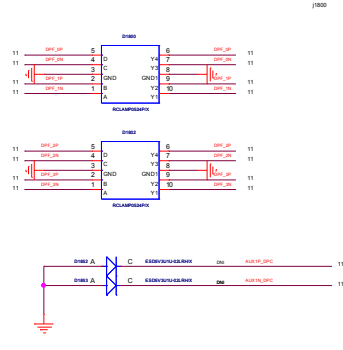
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Title ELLESMERE TMDP A/B			
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<b>GIGABYTE</b>			
Title: ELLESMERE TMDP C/D			
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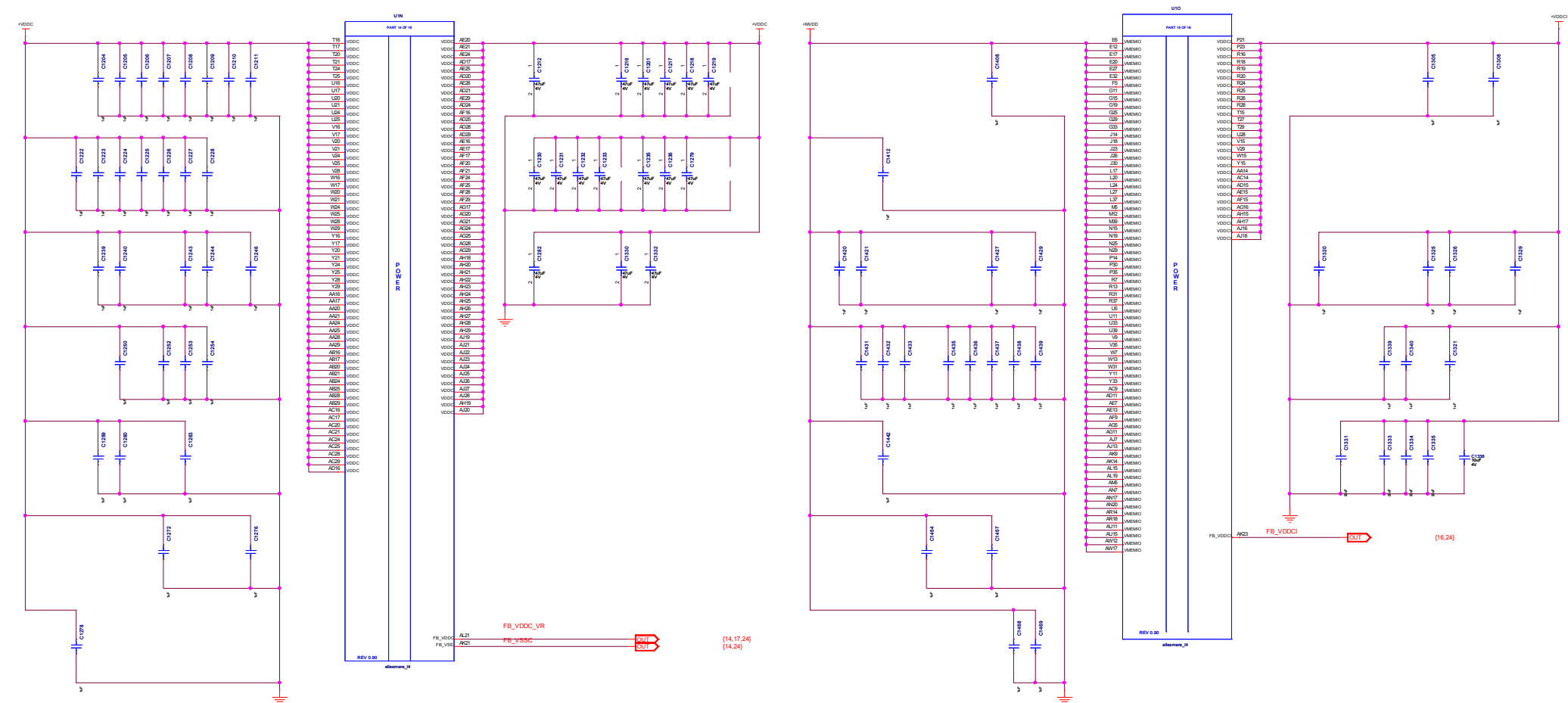


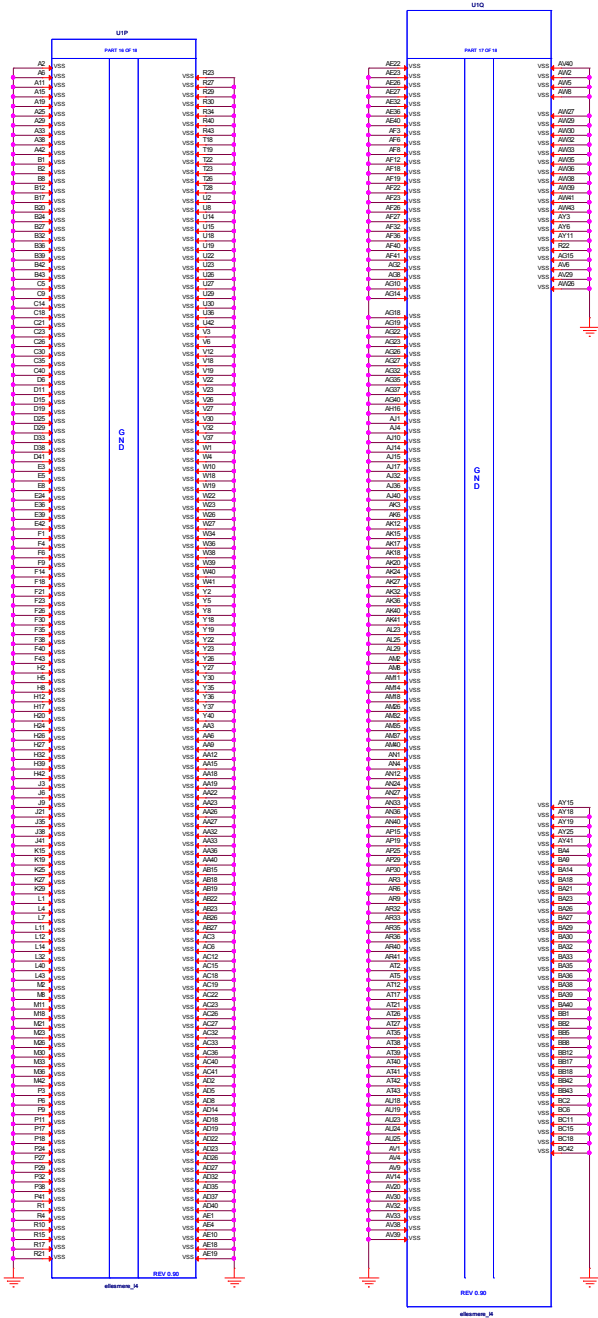
OPTIONAL ESD PROTECTION DIODES



GIGABYTE			
Title ELLESMERE LVTMDP E/F			
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(12) ELLESMERE POWER





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Title ELLESMERE GND			
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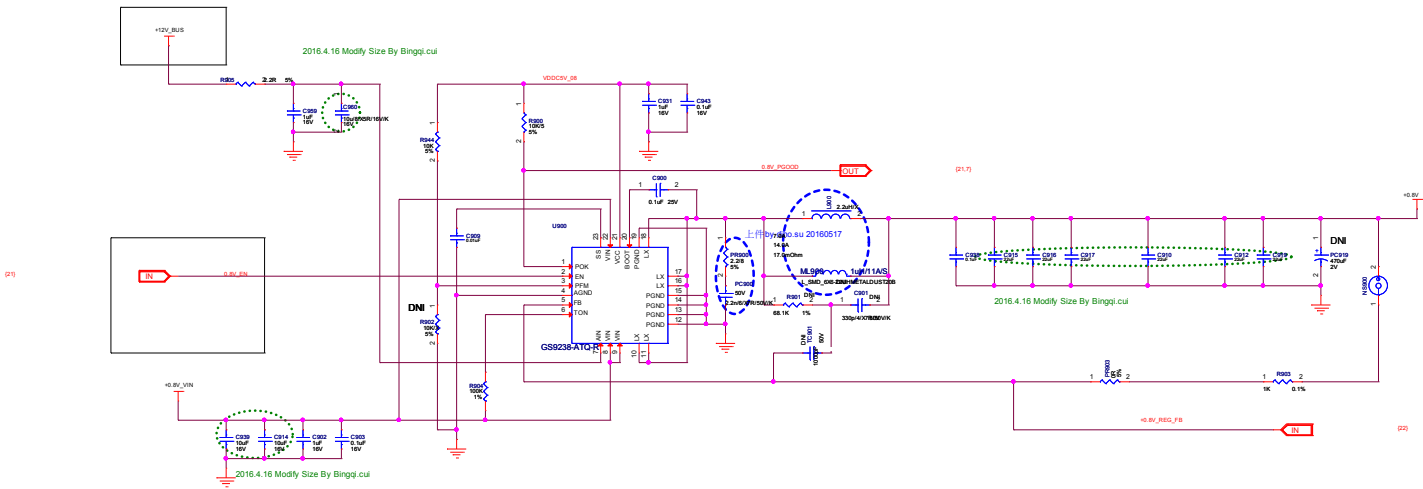






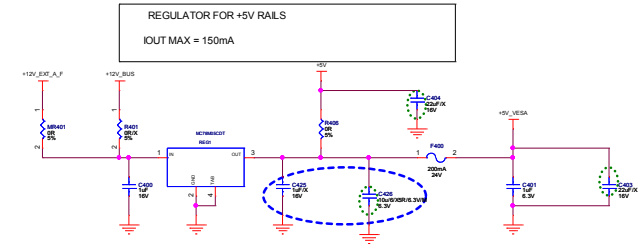
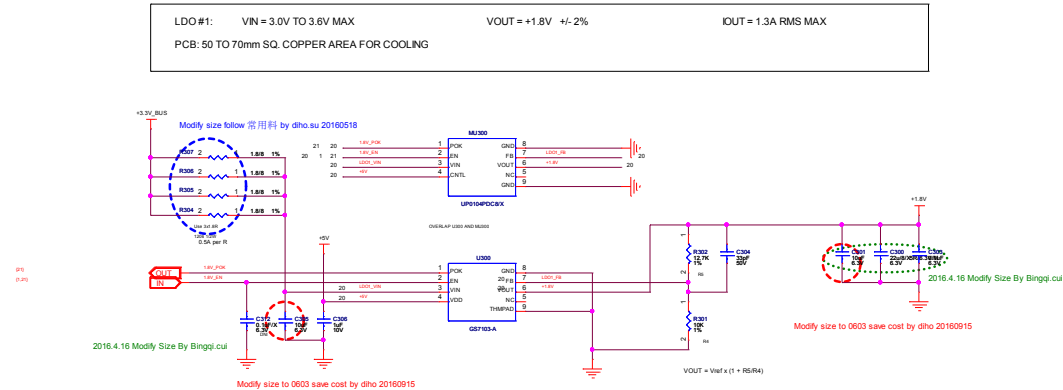


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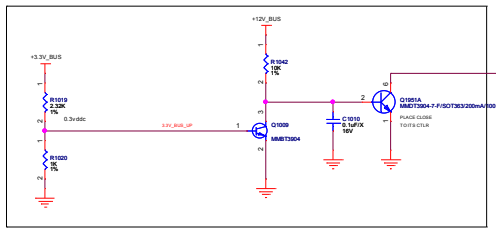
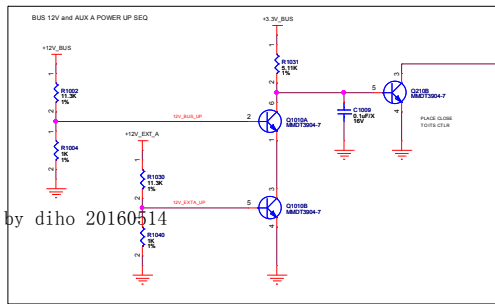
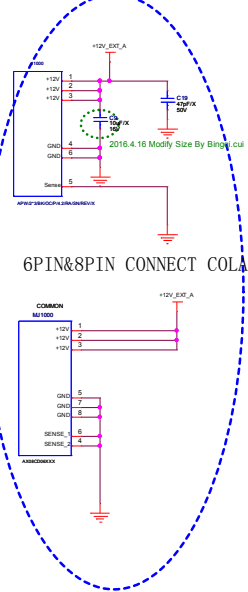
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Title		0.95V	
Size	Document Number	Rev	
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(18) SMALL RAIL REGULATORS

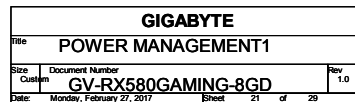
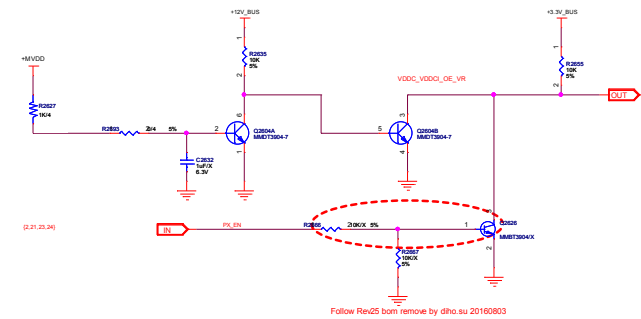
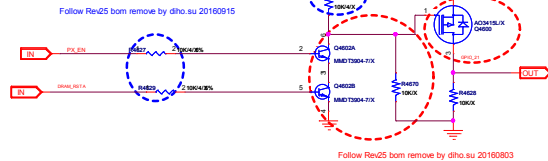
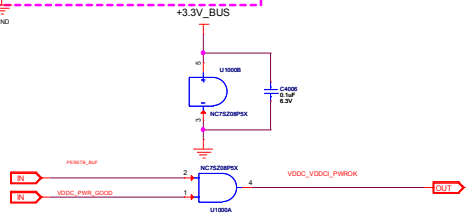
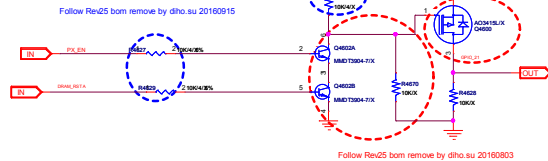
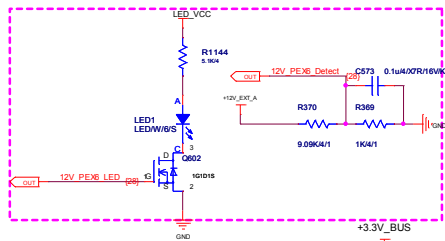
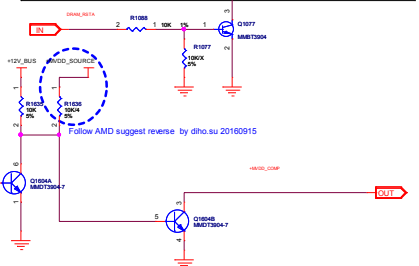
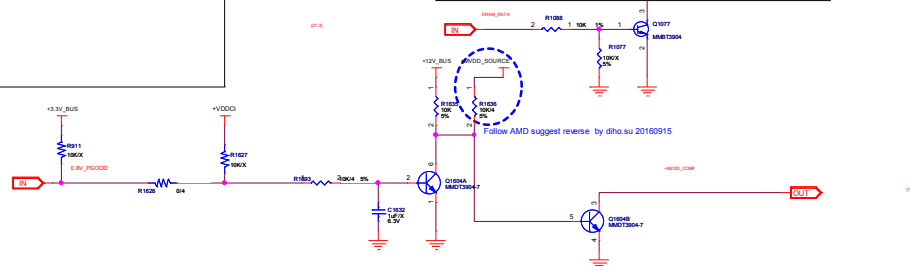
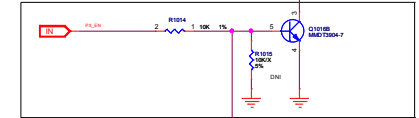
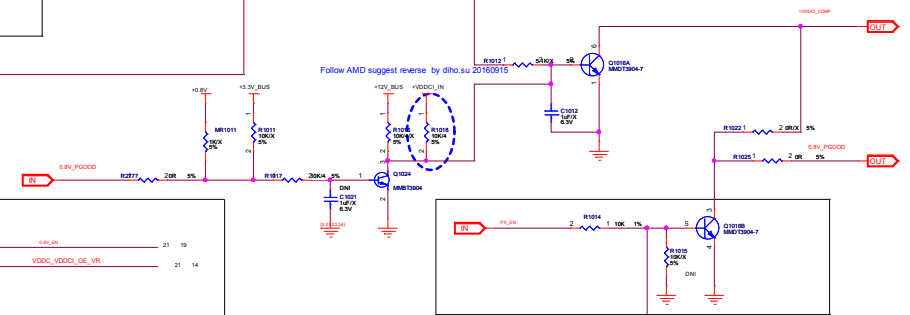
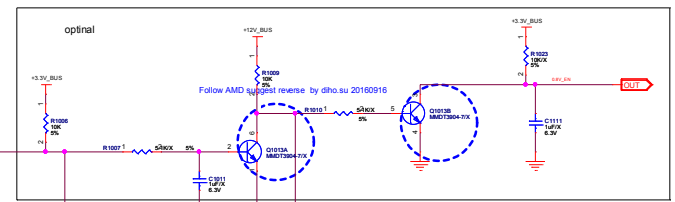
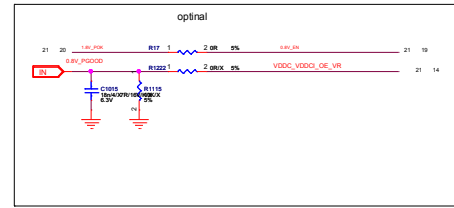


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(19) POWER MANAGEMENT

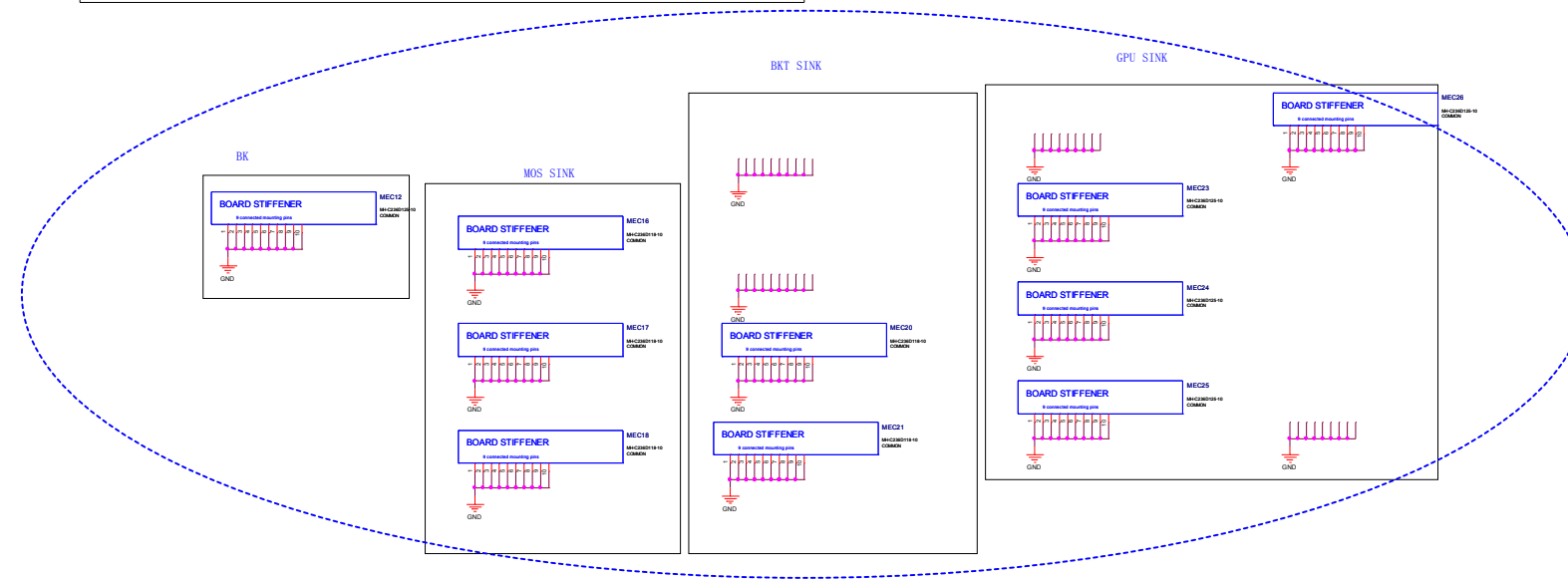
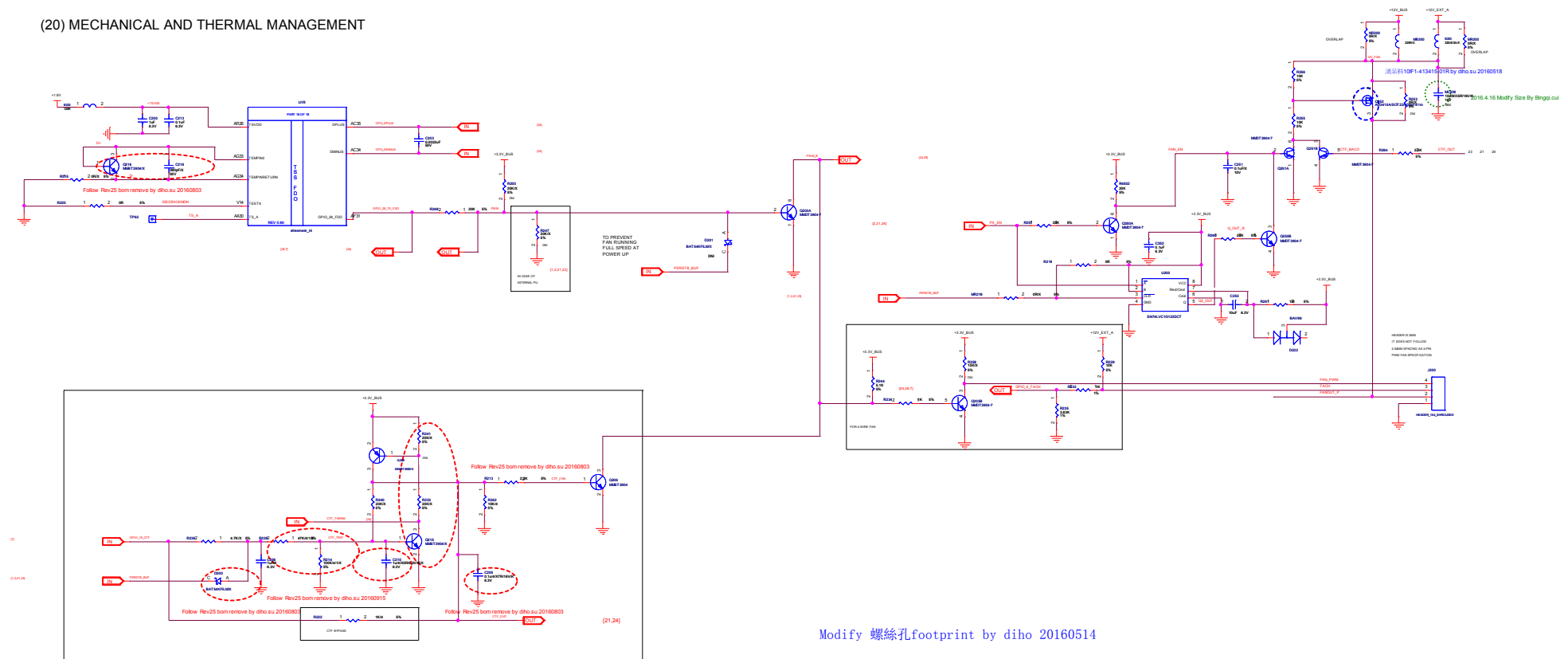


POWER UP SEQUENCE			
BUS ANALS (3.3V12V UP) → +1.8V → 0.930V	REF_VDDC VDDC → VDDCI NVSD		



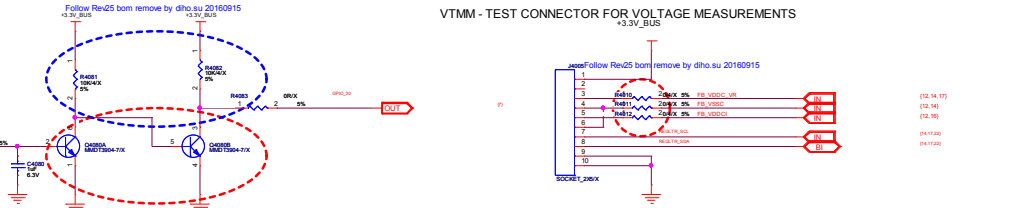
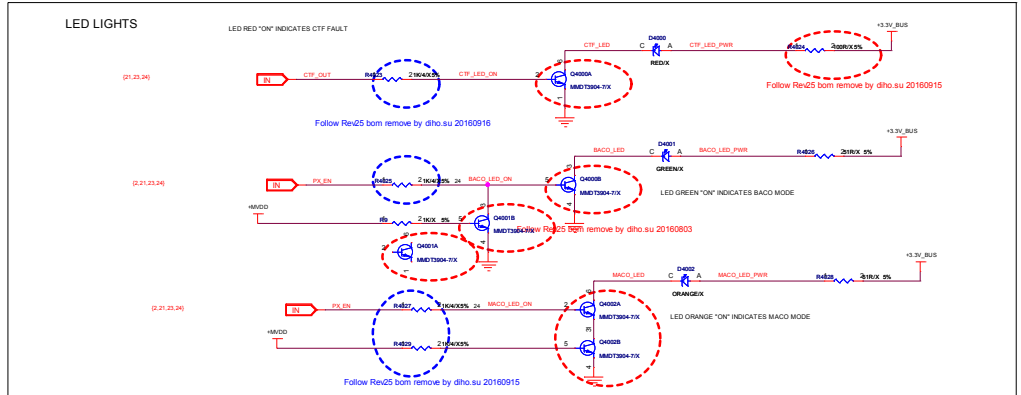
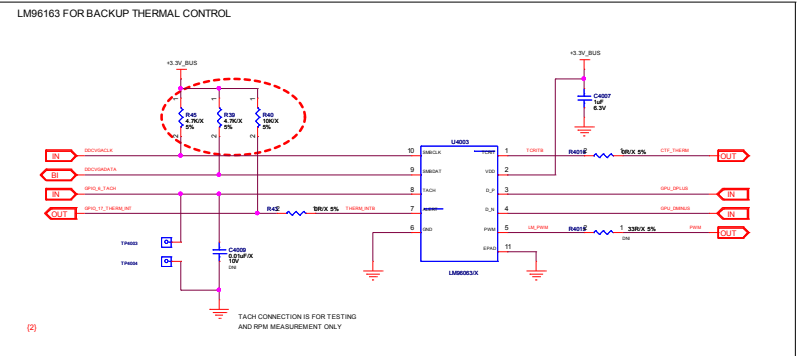
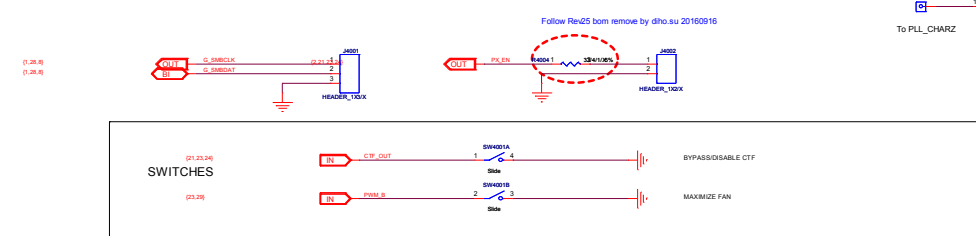
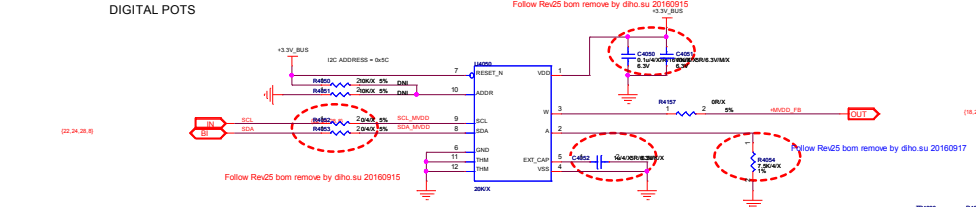
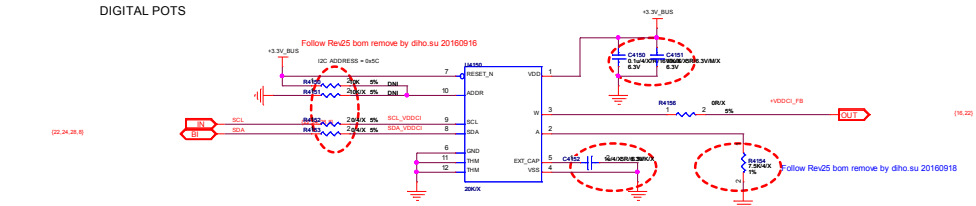
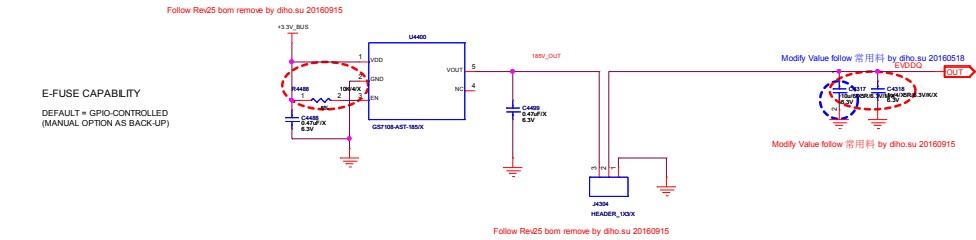
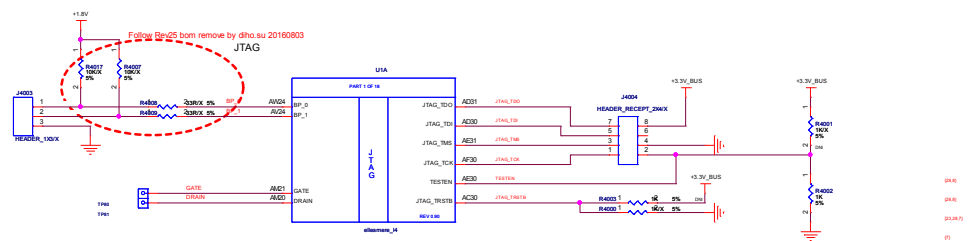


## (20) MECHANICAL AND THERMAL MANAGEMENT



GIGABYTE			
MECHANICAL AND THERMAL MANAGEMENT			
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Owner	GV-RX580GAMING-8GD	1.0	
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(21) DEBUG CIRCUITS



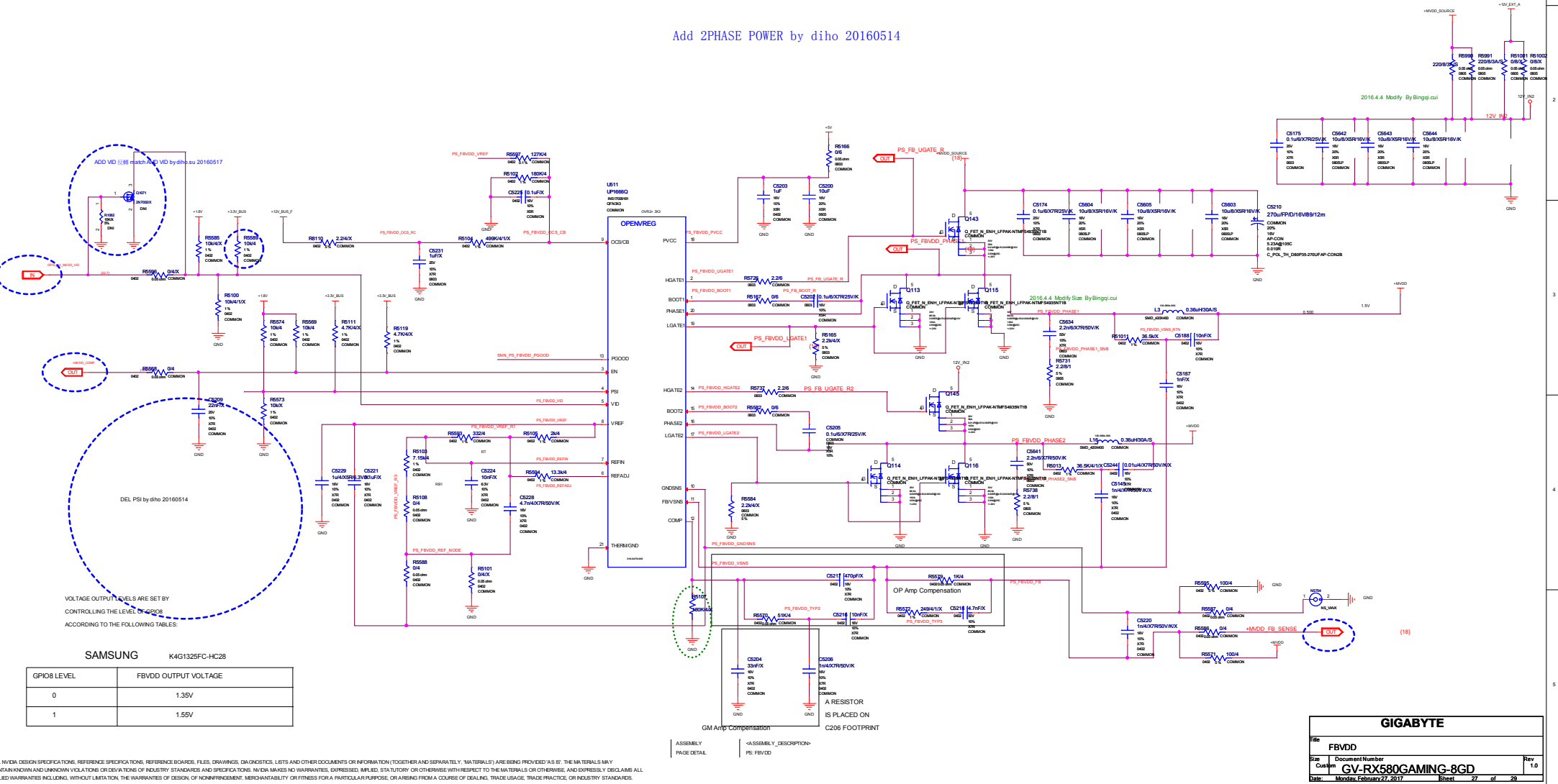
GIGABYTE			
DEBUG CIRCUITS			
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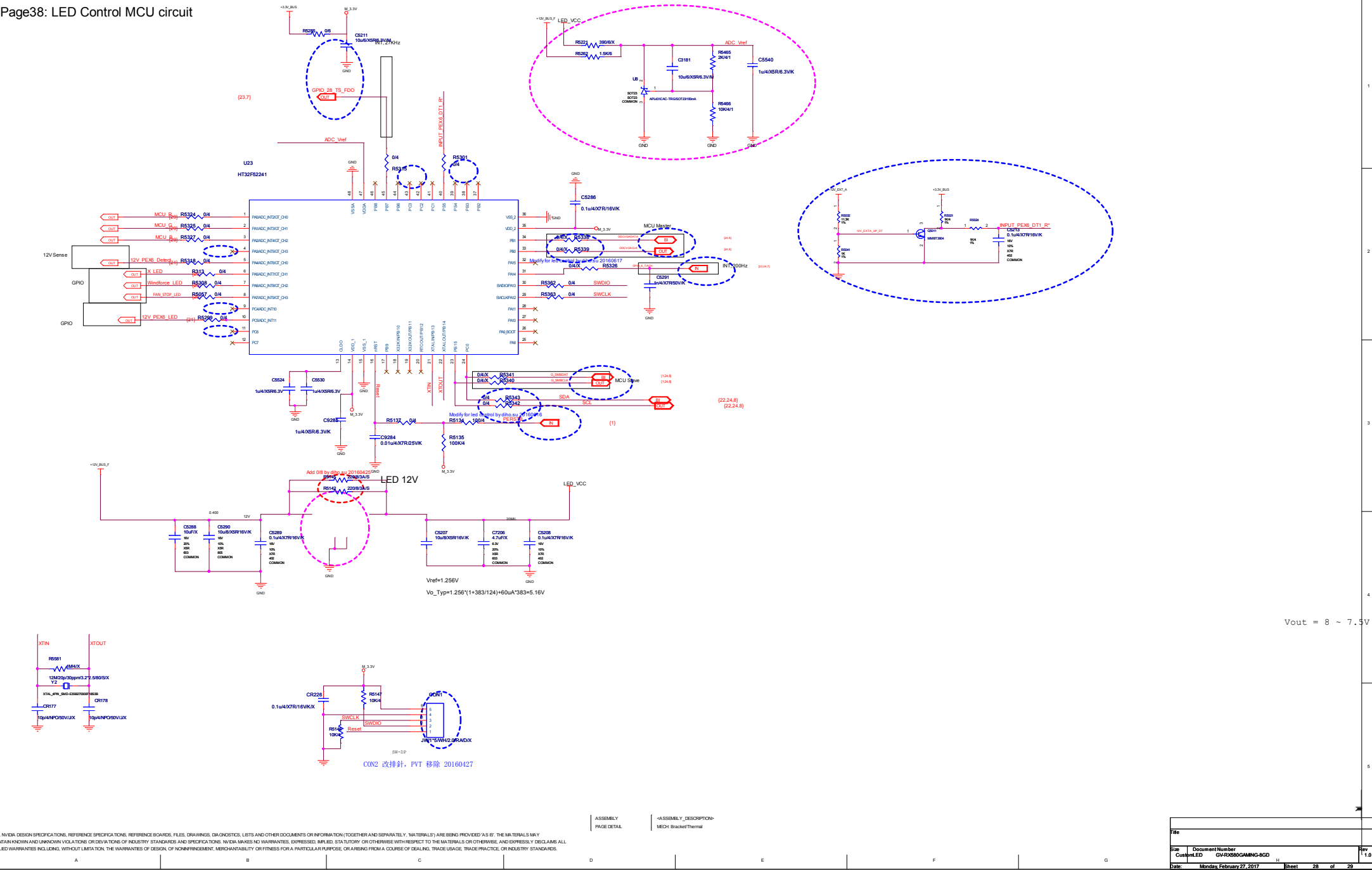




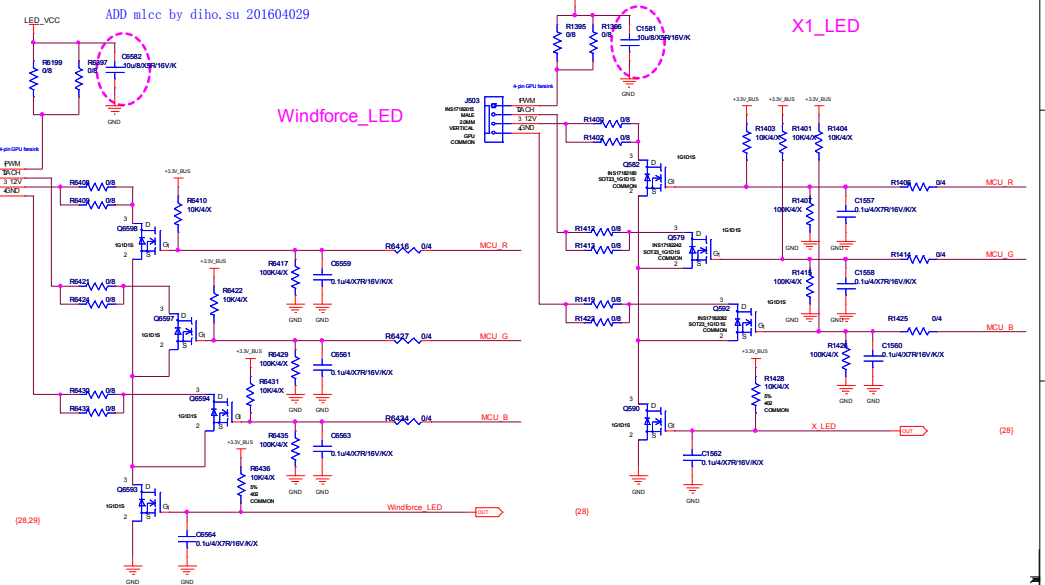
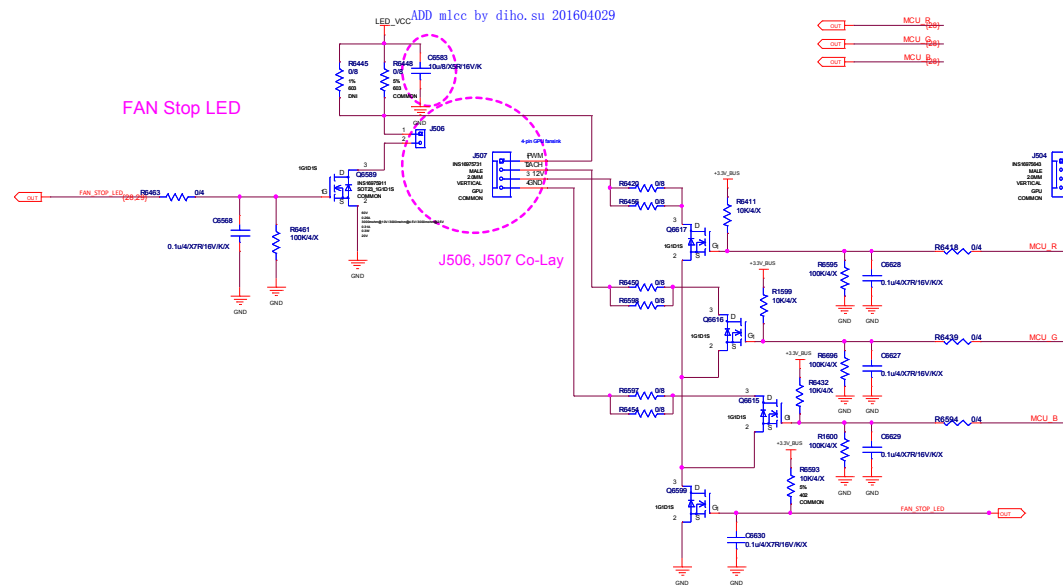
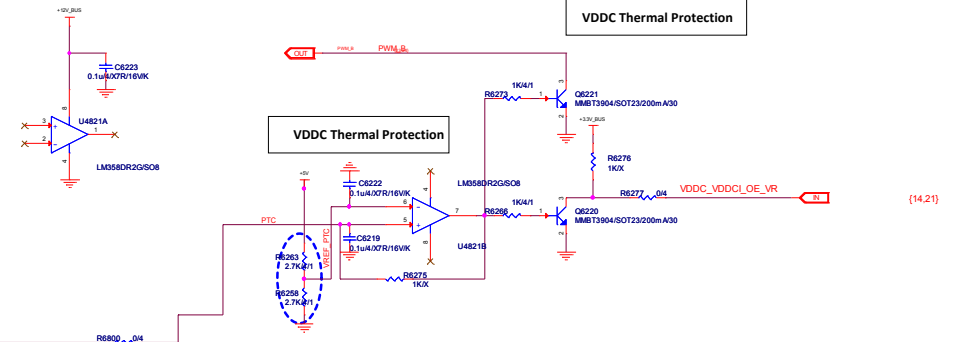
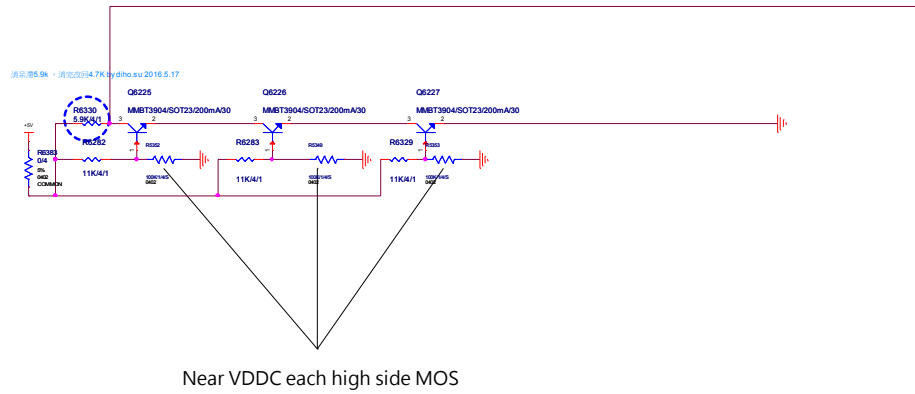
0	00A	00000000	
1	00B	00000000	1. Add SPIN1 to registers 10000 2. update 10000 when response
2	00C	00000000	PCSR: - add enable register 01000 - 01007 - add pull-down indicator 11000 - 11007 Remove C400, C403, V0400, C410, C41A, R400
3	00D	00000000	Modify "G07 Design"

GIGABYTE			
Title			
BLOCK DIAGRAM			
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## Add MOS VRHOT CIRCUIT



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